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Term	Documents
(9 AND 4).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	0
(L4 AND L9).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	0

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<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
<u>L21</u>	L4 and L9	0	<u>L21</u>
<u>L20</u>	L4 and L8	15	<u>L20</u>
<u>L19</u>	L4 and L7	57	<u>L19</u>
<u>L18</u>	L4 and L6	3	<u>L18</u>
<u>L17</u>	L4 and L5	56	<u>L17</u>
<u>L16</u>	L11 and L9	0	<u>L16</u>
<u>L15</u>	L11 and L8	0	<u>L15</u>
<u>L14</u>	L11 and L7	13	<u>L14</u>
<u>L13</u>	L11 and L6	2	<u>L13</u>
<u>L12</u>	L11 and L5	40	<u>L12</u>
<u>L11</u>	context\$3 near8 (swap\$5 or switch\$5) near12 (instruction\$1 or program\$1) near15 (specif\$6 or designt\$5 or parameter\$1)	103	<u>L11</u>

DB=PGPB,USPT; PLUR=YES; OP=OR

<u>L10</u>	(717/150,160)[CCLS]	231	<u>L10</u>
<u>L9</u>	(717/150,160)![CCLS]	231	<u>L9</u>
<u>L8</u>	(717/120-167)[CCLS]	6626	<u>L8</u>
<u>L7</u>	(718/102-108)[CCLS]	3881	<u>L7</u>
<u>L6</u>	(712/227,225)[CCLS]	1143	<u>L6</u>
<u>L5</u>	(712/2-300)[CCLS]	11961	<u>L5</u>

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<u>L4</u>	L3 near45 thread\$3	160	<u>L4</u>
<u>L3</u>	context\$3 near8 (swap\$5 or switch\$5) near12 (instruction\$1 or program\$1)	1438	<u>L3</u>

DB=USPT; PLUR=YES; OP=OR

<u>L2</u>	L1 and (loop\$4 or parallel\$6)	2	<u>L2</u>
<u>L1</u>	4868735.pn. or 5872963.pn.	2	<u>L1</u>

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IEEE JNL IEEE Journal or Magazine

IEEE JNL IEEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEEE CNF IEEE Conference Proceeding

IEEE STD IEEE Standard

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- ☐ 1. **The Weaves runtime framework**
 Srinidhi Varadarajan;
[Parallel and Distributed Processing Symposium, 2004. Proceedings. 18th International](#)
 26-30 April 2004 Page(s):197
 Digital Object Identifier 10.1109/IPDPS.2004.1303219
[AbstractPlus](#) | Full Text: [PDF](#)(1361 KB) IEEE CNF
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- ☐ 2. **MT ARM: multithreading implementation in Arm7 architecture**
 Cui Guangzuo; Li Zhaolin;
[ASIC, 2001. Proceedings. 4th International Conference on](#)
 23-25 Oct. 2001 Page(s):793 - 796
 Digital Object Identifier 10.1109/ICASIC.2001.982682
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